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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/500,759	07/06/2004	Dominicus Martinus Wilhelmus Leenaerts	NL 020002	7176

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PHILIPS INTELLECTUAL PROPERTY & STANDARDS  
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BRIARCLIFF MANOR, NY 10510

EXAMINER

NGUYEN, TUAN HOANG

ART UNIT PAPER NUMBER

2643

DATE MAILED: 01/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	10/500,759	LEENAERTS ET AL.	
	Examiner	Art Unit	
	Tuan H. Nguyen	2643	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 06 July 2004.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07/06/2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |   |   |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)             | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

## **DETAILED ACTION**

### ***Priority***

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### ***Drawings***

2. The drawings are objected to because of the following informalities: the illustration of Fig. 1, item 14 should change from "exteral" to "external". Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet"

pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Doetsch et al. (US PAT. 6,847,812 hereinafter, "Doetsch").

Regarding claim 1, Doetsch discloses a method for providing clock signals (item 10) to a mixed signal (items 20 and 23) telecommunication chip (items 1 and 11) having a communication signal in a communication signal band (col. 3 lines 27-55), said clock signals comprising a central clock frequency signal (system clock) and sub-frequency signals (sampling frequency) which are multiples or divisions (item 22) of said central clock frequency signal, wherein the central clock frequency signal is selected such that the central clock frequency signal and the sub-frequency signals are located outside the telecommunication signal band (Fig. 1 col. 3 line 27 through col. 6 line 41).

Regarding claims 2 and 8, Doetsch further discloses the telecommunication signal band is between 2,402 GHz and 2,480 GHz (col. 4 lines 31-41, the range is difference from system to system).

Regarding claims 3 and 9, Doetsch further discloses the central clock frequency is a multiple of 2 (col. 4 lines 31-38, "multiple of 2" read on "selecting suitable values").

Regarding claims 4 and 10, Doetsch further discloses the central clock frequency is in between 70 MHz and 90 MHz, preferably 64 MHz, 76 MHz or 80 MHz (col. 4 lines 12-20, the range is difference from system to system).

Regarding claims 5 and 11, Doetsch further discloses the central clock frequency is 64 MHz (col. 4 lines 12-20), the precision is 8 bit and the over-sampling factor is 32 (col. 4 lines 12-20).

Regarding claim 6, Doetsch further discloses the chip comprising functional circuit blocks, wherein the central-clock-frequency is supplied to the functional blocks by an on-chip oscillator (col. 4 lines 31-41).

Regarding claim 7, Doetsch further discloses mixed signal telecommunication chip comprising as functional blocks a RF front end unit (item 11), an analog to digital

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converter (item 6), a digital to analog converter (item 7), a modulator (item 5) /demodulator (item 3), a RF synthesizer (item 22) and an oscillator (item 8) for processing a communication signal in a communication signal band (col. 3 lines 25-55), wherein the functional blocks are fed by clock signals (item 10) comprising a central clock frequency signal (system clock) and sub-frequency (sampling frequency) signals which are multiples or divisions (item 22) of said central clock frequency signal (col. 3 lines 48-52), and wherein the central clock frequency signal and the sub-frequency signals are located outside of the telecommunication signal band (Fig. 1 col. 3 line 27 through col. 6 line 41).

Regarding claim 12, Doetsch further discloses the D/A converter (item 7) is directly connected to the RF front end (item 11) portion (Fig. 1 col. 3 lines 27-55).

Regarding claim 13, Doetsch further discloses an on-chip-oscillator supplying the central clock frequency to the functional blocks (Fig. 1 col. 3 lines 48-52).

Regarding claim 14, Doetsch further discloses the on-chip oscillator is connected to an external oscillator (Fig. 1 col. 2 lines 32-49).

Regarding claim 15, Doetsch further discloses the external oscillator is a crystal oscillator (Fig. 1 col. 6 lines 1-4).

***Conclusion***

5. Any response to this action should be mailed to:

Mail Stop\_\_\_\_\_ (Explanation, e.g., Amendment or After-final, etc.)

Commissioner for Patents

P.O. Box 1450

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Facsimile responses should be faxed to:

(571) 273-8300

Hand-delivered responses should be brought to:

Customer Service Window

Randolph Building

401 Dulany Street


Alexandria, VA 22313

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan H. Nguyen whose telephone number is (571) 272-8329. The examiner can normally be reached on 8:00Am - 5:00Pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Curtis Kuntz can be reached on (571) 272-7499. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Tuan Nguyen  
Examiner  
Art Unit 2643



CURTIS KUNTZ  
SUPERVISORY PATENT EXAMINER  
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